

10/586352

IAP6 Rec'd PCT/PTO 17 JUL 2006

00862.109538.

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | |
|--|---|----------------------------|
| In re Application of: |) | |
| | : | Examiner: Not Yet Assigned |
| MITSURU MAEDA |) | |
| | : | |
| Appln. No. N.Y.A. |) | Art Unit: Not Yet Assigned |
| (National Phase of International Application | : | |
| No. PCT/JP2005/004721, |) | |
| filed: March 10, 2005 | : | |
| |) | |
| Filed: HERewith | : | |
| |) | |
| For: ENCODING APPARATUS, | : | |
| ENCODING METHOD, |) | |
| DECODING APPARATUS, AND | : | |
| DECODING METHOD |) | July 14, 2006 |

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and in accordance with the practice under 37 C.F.R. §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed Form PTO-1449. Copies of the listed documents are also enclosed.

Each of the listed foreign documents was cited in at least one of an International Search Report and a Written Opinion of the International Searching Authority (copies enclosed) during prosecution of International Application No. PCT/JP2005/004721.

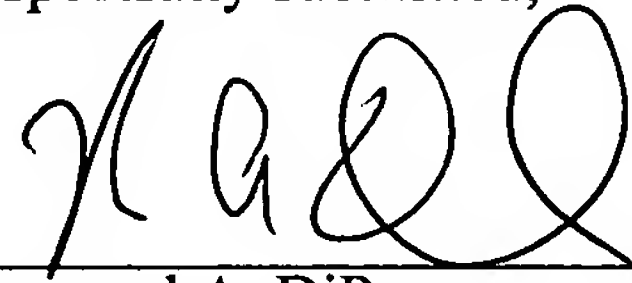
10/586352

IAP6 Rec'd PCT/PTO 17 JUL 2006

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be returned indicating that such information has been considered.

Applicant's undersigned attorney may be reached in our New York office by telephone at (212) 218-2100. All correspondence should continue to be directed to our address given below.

Respectfully submitted,



Raymond A. DiPerna
Attorney for Applicant
Registration No.: 44,063

FITZPATRICK, CELLA, HARPER & SCINTO
30 Rockefeller Plaza
New York, New York 10112-3800
Facsimile: (212) 218-2200

| | | | | | | | |
|---|----|---|---------|---|-------|--|---------------------------------------|
| FORM PTO 1449 (modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF REFERENCES CITED BY APPLICANT(S) (Use several sheets if necessary) | | | | ATTY DOCKET NO. 00862.109538. | | INTERNATIONAL APPLICATION NO. PCT/JP2005/004721, filed 3/10/05 | |
| | | | | APPLICANT MITSURU MAEDA | | 10/586352 | |
| | | | | FILING DATE HEREWITH | | GROUP NOT YET ASSIGNED | |
| U.S. PATENT DOCUMENTS | | | | | | | |
| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| FOREIGN PATENT DOCUMENTS | | | | | | | |
| | | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION YES/NO/ OR ABSTRACT |
| | EP | 0 658 057 A2 | 6/14/95 | EPO | | | English |
| | EP | 0 658 057 A3 | 6/19/96 | EPO | | | English |
| | WO | 01/26379 A1 | 4/12/01 | PCT | | | English |
| | | | | | | | |
| | | | | | | | |
| OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | |
| | | Zhang et al: "Dynamic Selection and Effective Compression of Key Frames for Video Abstraction", Pattern Recognition Letters, vol. 24, no. 9-10, pp. 1523-1532, June 2003. | | | | | |
| | | Sikora T: "MPEG Standards Section 1 Digital Video Coding Standards", Digital Consumer Electronics Handbook, pp. 803-823, I, 1997. | | | | | |
| | | Tudor P.N.: "Tutorial MPEG-2 Video Compression", Electronics and Communication Engineering Journal, Institution of Electrical Engineers, vol. 7, No. 6, pp. 257-264, December 1995. | | | | | |
| | | Tambankar A., et al.: "An overview of H.264/MPEG-4 part 10", Video/Image Processing and Multimedia Communications, pp. 1-51, July 2003. | | | | | |
| | | Yi J. Liang et al.: "Low-Latency video Transmission Over Lossy Packet Networks Using Rate-Distortion Optimized Reference Picture Selection", Proceedings 2002 International Conference on Image Processing, vol. 2 of 3, pp. 181-184, September 22, 2002. | | | | | |
| | | Chang A., et al.: "A Novel Approach to Fast Multi-Frame Selection for H.264 Vider coding", Proceedings of the 2003 of the 2003 IEEE Symposium on Circuits and Systems, vol. 2 of 5, pp. II-704-II-707, May 25, 2003. | | | | | |
| | | | | | | | |
| EXAMINER | | | | DATE CONSIDERED | | | |

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.